

## **IN THE SPECIFICATION**

Please replace paragraph 0085 with the following amended version:

--Referring now to FIG. 9, a block diagram of a PCI Express packet 900 is shown. The details of each of the blocks in the PCI Express packet 900 are thoroughly described in the PCI Express Base specification 1.0a (a specification that does not include provisions for sharing I/O) published by [www.pcisig.com](http://www.pcisig.com), which is incorporated herein by reference for all purposes. Additional information may be found in the texts referenced above with respect to FIG. 2C.--